## WHAT IS CLAIMED IS:

1. In a traffic control system for coordinated operation of a plurality of traffic control lights and pedestrian advisory signs having potentially conflicting states; a malfunction management unit having input terminals for receiving control signals used to operate the traffic control lights and pedestrian advisory signs, monitoring means for detecting a conflict between a flashing DON'T WALK input signal and other traffic control signals; and an output for controlling the operation of an output relay used to transfer the operation of the traffic control lights to a flashing mode of operation when a conflict is detected.

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- 2. The invention of claim 1 wherein said malfunction management unit includes a manually settable switch for enabling and disabling said monitoring means.
- 3. The invention of claim 1 wherein said malfunction management unit includes a display for indicating whether said monitoring means is enabled.
  - 4. The invention of claim 3 wherein said control signals are assigned to channels; and wherein said display includes a plurality of display units assigned to different channels to indicate those channels for which said monitoring means is enabled.

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- 5. A method of monitoring for conflicts between flashing DON'T WALK pedestrian advisory sign control signals and other control signals used to operate traffic control lights, said method comprising the steps of:
- (d) detecting a flashing DON'T WALK pedestrian advisory sign control signal;
- 25 (e) detecting the states of the other control signals;
  - (f) generating a fault signal when a conflict occurs between a flashing DON'T WALK signal and the other control signals.
  - 6. The invention of claim 5 wherein said pedestrian advisory sign control signals and said other control signals are grouped in a plurality of channels; and wherein said method further includes the step of providing a display of those channels on which said steps (a) and (b) are enabled.
  - 7. The invention of claim 5 further including the step of manually enabling the performance of steps (a) and (b).